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## OFD - DENNIS CAROLYN

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~~Rtems Smp For Leon3 Leon4 Multi Processor Devices~~

RTEMS SMP for LEON3/LEON4 Multi-Processor Devices. When multi-core processors are used in the space industry, they are mostly used in AMP configurations. The cost of increased complexity and difficulty in analyzing SMP systems has been deemed too high in comparison with the benefits of more processing power.

*Current and Next Generation LEON SOC Architectures for Space, Jan Andersson, Aeroflex Gaisler*  
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Synopsys Design Compiler Synthesis Lecture (2013) **Leon How to calculate CRC (Cyclic Redundancy Check)** \*this is by Leon/ Mellow again\* day 4 *Playing Leon Leon LeOn In The IEaD Rtems Smp For Leon3 Leon4* ~~ESA Microprocessors~~ Download Citation | Rtems SMP for LEON3/LEON4 multi-processor devices | When multi-core processors are used in the space

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Tel: +46 31 7758650,  
 Fax: +46 31 421407  
 fdaniel.cederman,  
 daniel.hellstromg@gaisler  
 .com

~~ESA RTEMS SMP Improvement for LEON multi-core~~

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This eases the use of CPU counter timestamps in external tools which do not know the details of the CPU counter hardware. The CPU counter is the fastest way to get a time on an RTEMS system. Such a CPU counter may be also used as the time-counter. Use it on SPARC for this purpose to simplify the clock drivers. Update #3456.

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The objective of the RTEMS-SMP Improvement for

LEON multi-core activity was to improve the prototype SMP implementation from the earlier studies to production level quality, such it can be used in future space missions, in particular in the context of payload software, running on either the GR712RC (LEON3 dual core) or the GR740 (LEON4 quad core).

Last change on this file since c499856 was c499856, checked in by Chris Johns <chrisj@...>, on Mar 20, 2014 at 9:10:47 PM; Change all references of rtems.com to rtems.org. Property mode set to 100644; File size: 2.0 KB

Last change on this file since ca63ae2 was ca63ae2, checked in by Sebastian Huber <sebastian.huber@...>, on Jun 13, 2013 at 1:41:21 PM; smp: Add and use \_CPU\_SMP\_Send\_interrupt() Delete bsp\_smp\_interrupt\_cpu(). Property mode set to 100644; File size: 3.6 KB

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Newlib standalone C-library RCC allows cross-compilation of RTEMS C/C++ applications for LEON4, LEON3, LEON2 and ERC32. Using the gdb

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