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**High-Definition Video Reference Design (UDX5) Application Note**

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**1080p video framework from Altera**

Hot Plug Detect (all versions) HEAC— (HDMI 1.4+, optional, HDMI Ethernet Channel and Audio Return Channel) HDMI ( High-Definition Multimedia Interface) is a proprietary audio/video interface for transmitting uncompressed video data and compressed or uncompressed digital audio data from an HDMI-compliant source device, such as a display controller, to a compatible computer monitor, video projector, digital television, or digital audio

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The converted image is mixed and displayed over user-selectable output such as SDI, DVI, or high-definition multimedia interface (HDMI). The design uses Altera's intellectual property (IP) for SDI and video and image processing (VIP) functions.

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This reference design demonstrates the Intel FPGA High Definition Multimedia Interface (HDMI) 2.0 video connectivity IP core with a video processing pipeline based on IP cores from the Intel FPGA Video and Image Processing (VIP) Suite. This design is intended as a simple reference for interconnectivity between the HDMI IP core and the VIP Suite. Additionally, this design demonstrates the use of separate clocks for the RX and TX HDMI IP cores, allowing for differing RX and TX video resolutions.

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